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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/718,975
				Filing Date	November 20, 2003
				First Named Inventor	Ha C. Vu
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	08211/0200375-USO

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear ⁶

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
SS	CA	Marie, Herve, et al. "R, G, B Acquisition Interface with Line-Locked Clock Generator for Flat Panel Display", IEEE Journal of Solid-State Circuits, Vol. 33, No. 7, July 1998, pp. 1009-1013.		
SS	CB	Marie, Herve, et al. "Correction to 'R, G, B Acquisition Interface with Line-Locked Clock Generator for Flat Panel Display'", IEEE Journal of Solid-State Circuits, Vol. 35, No. 8, August 2000, p. 1253.		

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Examiner Signature	/Stephen Sherman/ (05/22/2006)	Date Considered	05/22/2006
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